Preliminary

* This is tentative specification

UVLO built-in IGBT gate driver with Power cut circuit

DESCRIPTION

RT8H112C is combined transistor composed by NPN transistors, PNP transistors and resistors.

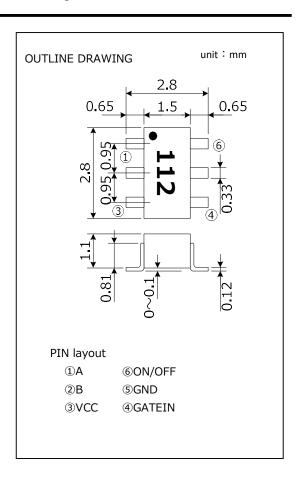
Miniaturization of the set, and significant reductions of parts and person-hours will be possible by using this transistor.

RT8H112C has a built-in UVLO circuit, and starts operation when the power supply voltage becomes about 16V or more, and stops operation when it becomes 14V or less. It has a circuit configuration as an IGBT driver, and when applying from Low to High to the GATEIN terminal, when the voltage becomes about 2.7V or more, the B terminal outputs a Low signal.

When High to Low is applied to the GATEIN terminal, when the voltage becomes about 2.5V or less, B terminal outputs a High signal.

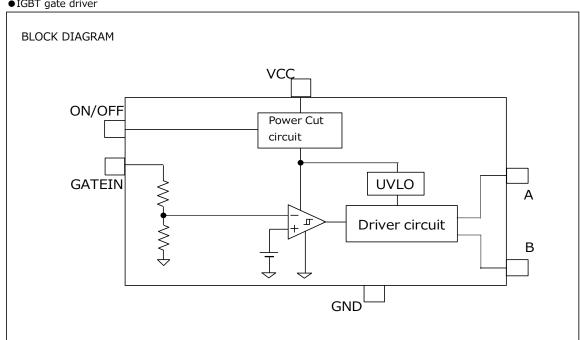
FEATURES

- Miniaturization of a set
- Built-in UVLO(Ideal for 20V systems)
- Since the output is constant current, the IGBT can be operated safely
- Built-in power cut circuit (ICCoff = 0 during power cut)



APPLICATION

●IGBT gate driver



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ABSOLUTE MAXIMUM RATINGS (Ta=25℃ unless otherwise noted.)

Symbol	Parameter Condition		Ratings	Unit
Vcc	Power supply voltage		30	V
VGIN	GATE IN terminal input applied voltage		-0.3~10	٧
VON/OFF	ON/OFF terminal input applied voltage		-0.3~10	V
Pd	Internal power dissipation		200	mW
Κθ	Thermal derating	Ta≧25℃	1.6	mW/℃
Tj	Junction temperature		150	\mathbb{C}
Tstg	Storage temperature	keep dry	-40~150	$^{\circ}$
Topr	Operating temperature	keep dry	-20~85	$^{\circ}$

ELECTRICAL CHARACTERISTIC (Ta=25°C,VCC=20V,VON/OFF=5V unless otherwise noted.)

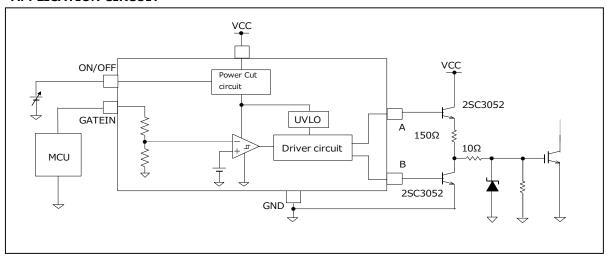
Cymphol	Parameter	Test condition		Limits		
Symbol				Тур.	Max.	Unit
VCC	Operating supply voltage range		18	20	26	V
VCTH1	Operation start voltage	GATEIN=5V、VMB:High→Low	15.0	16.1	17.2	V
VCTH2	Operation stop voltage	GATEIN=5V、VMB:Low→High	13.0	14.0	15.0	V
ICCoff	Circuit current	VON/OFF = 0 V		0	1	uA
	(During power cut)					
ICC1	Circuit current1	GATEIN=0V	0.91	1.30	1.69	mΑ
ICC2	Circuit current2	GATEIN=5V	0.92	1.31	1.70	mΑ
VOA2	Output voltage A2	GATEIN=5V	16.2	18.6	20.0	V
		Pull-down resistor(to A terminal):1MΩ				
VOB2	Output voltage B2	GATEIN=5V		0.07	0.28	V
		Pull-down resistor(to A terminal):1M Ω	_			
Vth0	Threshold voltage 1	GATEIN=0V、VMB:Low→High	0.50	0.85	1.20	V
	(Low→High)					
Vth1	Threshold voltage 1	GATEIN: 0→5V, VMB: High→Low	2.62	2.78	2.93	V
	(Low→High)	GATEIN: U-5V, VMB: HIGH-LOW		2.78	2.93	v
Vth2	Threshold voltage 2	GATEIN : 5V→0、VMB : Low→High		2.48	2.62	V
	(High→Low)					
IOUTA1	Output A outflow current 1	GATEIN=0V、A=B=0.7V IMA	-	0	1	uA
IOUTA2	Output A outflow current 2	GATEIN=5V、A=18V IMA	-1.50	-1.15	-0.80	mA
IINB	Output B inflow current	GATEIN=5V, B=0.3V IMB	870	1250	1620	uA
IOUTB	Output B outflow current	GATEIN=0V、B=0.7V IMB	-2.25	-1.73	-1.21	mA

Preliminary

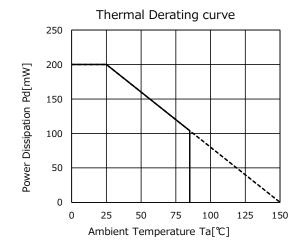
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APPLICATION CIRCUIT



«Typical Characteristic»



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