PRELIMINARY

INJ0503BC2

Notice: This is not a final specification Some parametric are subject to change.

Switching Silicon P-channel MOSFET

DESCRIPTION

INJ0503BC2 is a Silicon P-channel MOSFET.

This product is most suitable for use such as portable machinery, because of low voltage drive and low on resistance.

FEATURE

- •High drain current ID=-5.1A
- •Drive voltage −2.5V
- •Low on Resistance. RDS(ON)=35m $\Omega \text{ typ}(@VGS=-4.5V)$.

RDS(ON)=45m Ω typ(@VGS=-2.5V).

APPLICATION

Switching.

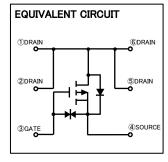
MAXIMUM RATINGS (Ta=25°C)

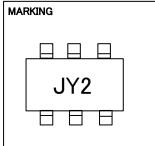
Parameter	Symbol	Rating	Unit	
Drain-Source Voltage	VDSS	-20	V	
Gate-Source Voltage	Vgss	±12	V	
Drain Current(DC) (%1)	ĪD	-5.1	Α	
Drain Current(Pulse) (%2)	I DP	-16	Α	
Total Power Dissipation (%1)	PD	1.5	W	
Channel Temperature	Tch	+150	°C	
Storage Temperature	Tstg	−55 ~ +150	°C	

X1 package mounted on glass-epoxy substrate.

(39mm × 39mm × 1.6mm, Cu pad 1500mm²)

 $\&2 \text{ Pw} \leq 10 \text{ms}$, Duty cycle $\leq 1\%$





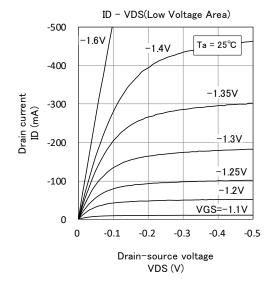
ELECTRICAL CHARACTERISTICS (Ta=25°C)

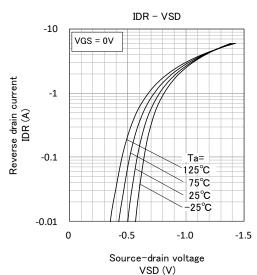
Parameter	Symbol	Test Condition	Limit			Unit
			MIN	TYP	MAX	Unit
Drain-Source Breakdown Voltage	V(BR)DSS	I_D =-250 μ A, V_{GS} =0V		-	-	٧
Gate-Source Leak Current	I gss	$V_{GS}=\pm 12V$, $V_{DS}=0V$		-	±25	μΑ
Zero Gate Voltage Drain Current	I DSS	V _{DS} =-20V, V _{GS} =0V	_	-	-1.0	μΑ
Gate Threshold Voltage	Vth	I_D =-250 μ A, V_{DS} = V_{GS}	-0.5	-	-1.2	٧
Static Drain-Source On-State Resistance	Rds(on)	I _D =-5.1A, V _{GS} =-4.5V	-	35	45	mΩ
		I _D =-5.1A, V _{GS} =-2.5V	-	45	60	mΩ
Input Capacitance	Ciss		-	1000	-	pF
Output Capacitance	Coss	V _{DS} =-10V, V _{GS} =0V, f=1MHz	-	130	-	
Feedback Capacitance	Crss		-	90	-	
Switching Time	ton	\\ - 20\\ I - 200-A \\ - 5\\	-	100	-	ns
	toff	V_{DD} =-20V, I_{D} =-200mA, V_{GS} =-5V	_	1050	-	

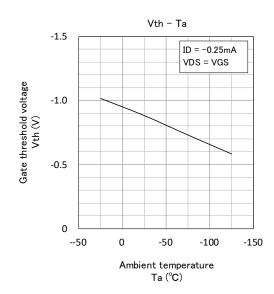
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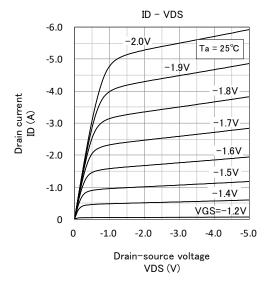
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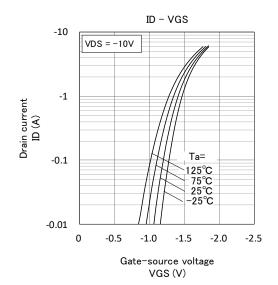
TYPICAL CHARACTERISTICS

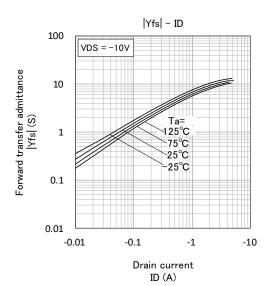






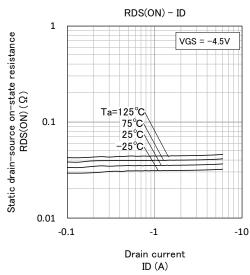


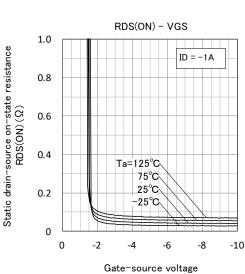




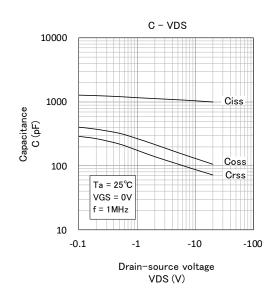
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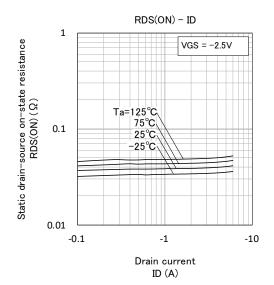
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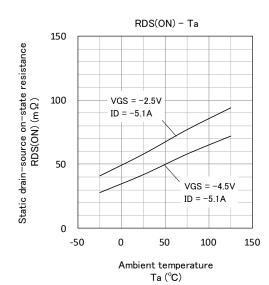


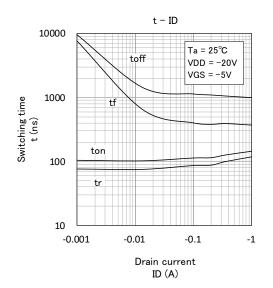


VGS (V)



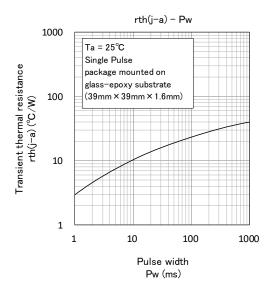


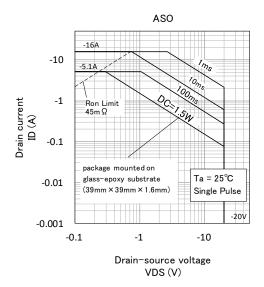


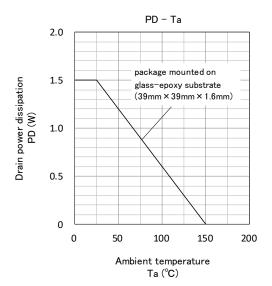


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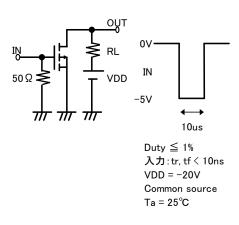
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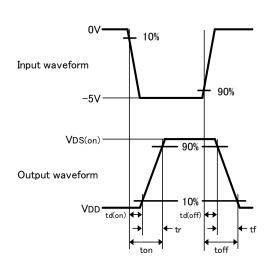






Switching time test condition





Keep safety first in your circuit designs!

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