INK040AAC1

Notice: This is not a final specification Some parametric are subject to change.

High Speed Switching Silicon N-channel MOSFET

DESCRIPTION

INK040AAC1 is a Silicon N-channel MOSFET.

This product is most suitable for use such as portable machinery, because of low voltage drive and low on resistance.

FEATURE

- •Input impedance is high, and not necessary to consider a drive electric current.
- High drain current ID=3.7A
- •Drive voltage 4V
- •Low on Resistance. RDS(ON)=48m Ω typ(@VGS=10V). $RDS(ON)=63m\Omega \ typ(@VGS=4.5V)$
- ·High speed switching.

APPLICATION

High speed switching, Analog switching

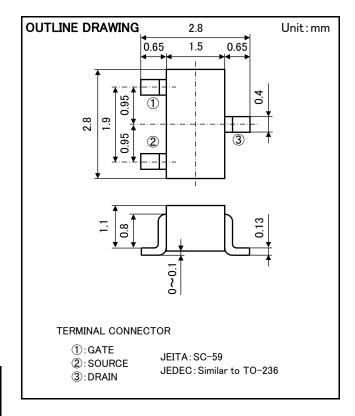
MAXIMUM RATINGS (Ta=25°C)

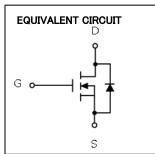
Parameter	Symbol	Rating	Unit
Drain-Source Voltage	VDSS	100	V
Gate-Source Voltage	Vgss	+20/-12	V
Drain Current(DC) (%1)	I D	3.7	Α
Drain Current(Pulse) (%2)	ĪDP	12	Α
Total Power Dissipation (%1)	Pb	0.9	W
Channel Temperature	Tch	+150	°C
Storage Temperature	Tstg	−55 ~ +150	°C

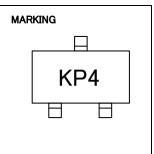
X1 package mounted on glass-epoxy substrate.

 $(39\text{mm} \times 39\text{mm} \times 1.6\text{mm},\text{Cu pad }1500\text{mm}^2)$

 $\fint 2$ Single pulse $Pw \le 10ms$, Duty cycle $\fint 1\%$







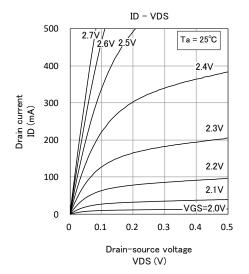
ELECTRICAL CHARACTERISTICS (Ta=25°C)

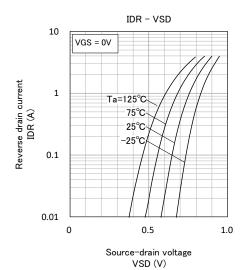
Parameter	Symbol	Test Condition		Limit		
			MIN	TYP	MAX	Unit
Drain-Source Breakdown Voltage	V(BR)DSS	I _D =250μA, V _{GS} =0V	100	_	_	٧
Gate-Source Leak Current	Igss	$V_{GS}=\pm 20V$, $V_{DS}=0V$	-	-	±1.0	μA
Zero Gate Voltage Drain Current	IDSS	V _{DS} =100V, V _{GS} =0V	-	-	1.0	
Gate Threshold Voltage	Vth	I _D =250μA, V _{DS} = V _{GS}	1.0	-	2.5	٧
Static Drain-Source On-State Resistance	Rds(on)	I _D =3.7A, V _{GS} =4.5V	-	63	83	mΩ
		I _D =3.7A, V _{GS} =10V	-	48	63	
Input Capacitance	Ciss	V _{DS} =10V, V _{GS} =0V, f=1MHz	-	635	-	pF
Output Capacitance	Coss		-	290	_	
Feedback Capacitance	Crss		-	72	-	
Switching Time	ton	V 2007 1 200 A V 5V	_	18	-	ns
	toff	V _{DD} =20V, I _D =200mA, V _{GS} =5V	-	30	-	

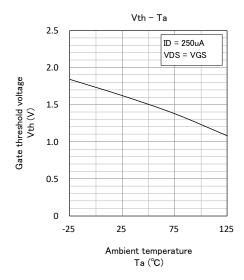
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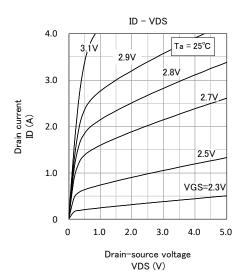
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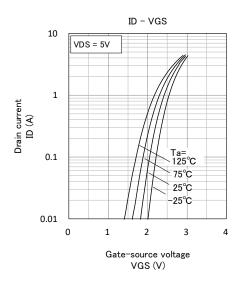
TYPICAL CHARACTERISTICS

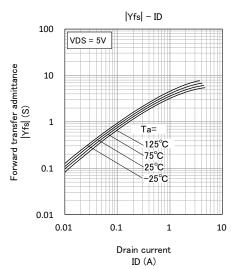






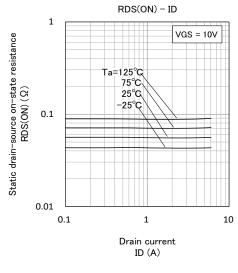


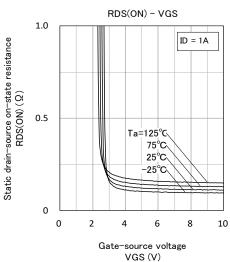


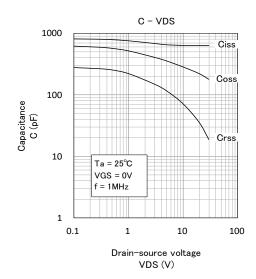


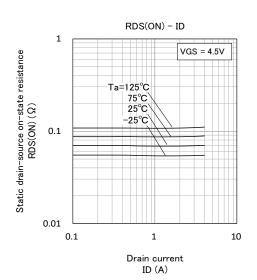
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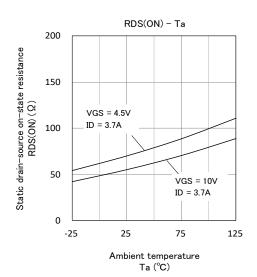
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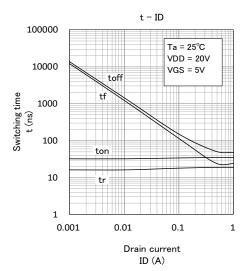






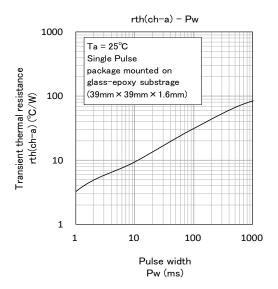


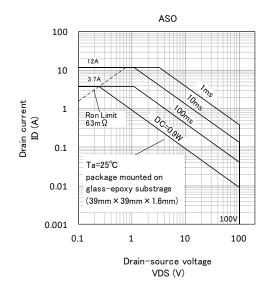


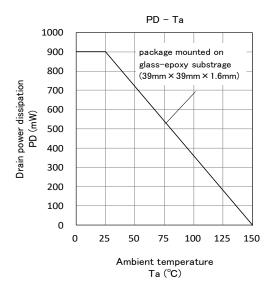


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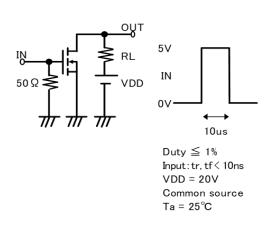
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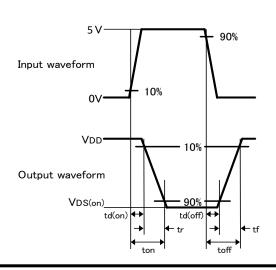






Switching time test condition





Keep safety first in your circuit designs!

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