High Speed Switching Silicon N-channel MOSFET

DESCRIPTION

INK0612AC1 is a Silicon N-channel MOSFET.

This product is most suitable for use such as portable machinery, because of low voltage drive and low on resistance.

FEATURE

- •Input impedance is high, and not necessary to consider a drive electric current.
- ·High drain current ID=6A
- •Drive voltage 4V
- •Low on Resistance. RDS(ON)=22m Ω typ(@VGS=10V). RDS(ON)=24m Ω typ(@VGS=4.5V).
- · High speed switching.

APPLICATION

High speed switching, Analog switching

MAXIMUM RATINGS (Ta=25°C)

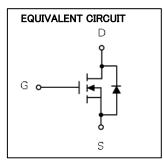
Parameter	Symbol	Rating	Unit	
Drain-Source Voltage	VDSS	30	٧	
Gate-Source Voltage	Vgss	±20	٧	
Drain Current(DC)(%1)	ĪD	6	Α	
Drain Current(Pulse) (%2)	I DP	12	Α	
Total Power Dissipation (※1)	PD	0.9	W	
Channel Temperature	Tch	+150	°C	
Storage Temperature	Tstg	−55 ~ +150	°C	

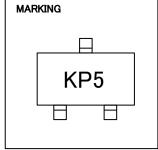
 $\frak{\%}1$ package mounted on glass-epoxy substrate.

 $(39\text{mm}\times39\text{mm}\times1.6\text{mm},\text{Cu pad }1500\text{mm}^2)$

 $\frak{\%}2$ Single pulse $Pw\frak{\le}10ms$, Duty cycle $\frak{\le}1\%$

OUTLINE DRAWING 2.8 0.65 1.5 0.65 1.5 0.65 TERMINAL CONNECTOR ①: GATE ②: SOURCE ③: DRAIN DITERMINAL TO TO 236



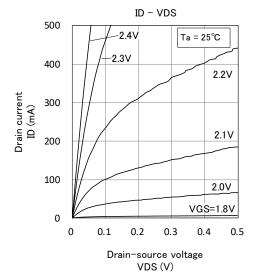


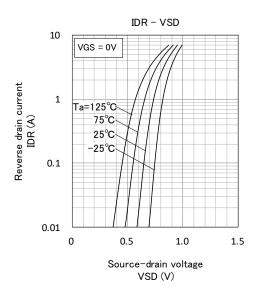
ELECTRICAL CHARACTERISTICS (Ta=25°C)

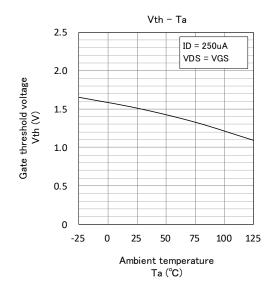
Parameter	Symbol	Test Condition		Limit			
			MIN	TYP	MAX	Unit	
Drain-Source Breakdown Voltage	V(BR)DSS	I _D =250μA, V _{GS} =0V	30	_	-	٧	
Gate-Source Leak Current	Igss	$V_{GS}=\pm 20V$, $V_{DS}=0V$	-	_	±1.0	μΑ	
Zero Gate Voltage Drain Current	IDSS	V _{DS} =30V, V _{GS} =0V	-	-	1.0	μA	
Gate Threshold Voltage	Vth	I _D =250μA, V _{DS} = V _{GS}	1.0	-	2.5	٧	
Static Drain-Source On-State Resistance	Rds(on)	I _D =6A, V _{GS} =4.5V	-	22	33	0	
		I _D =6A, V _{GS} =10V	-	24	40	mΩ	
Input Capacitance	Ciss		-	695	-	pF	
Output Capacitance	Coss	V _{DS} =10V, V _{GS} =0V, f=1MHz	-	120	-		
Feedback Capacitance	Crss		-	95	-		
Switching Time	ton	V 20V I 200 A V 5V	-	33	-	ns	
	toff	V _{DD} =20V, I _D =200mA, V _{GS} =5V	_	85	_		

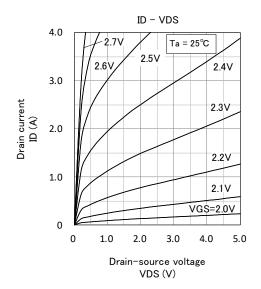
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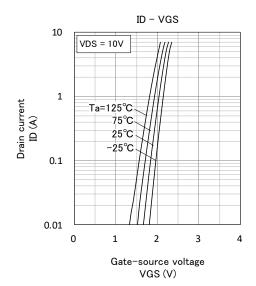
TYPICAL CHARACTERISTICS

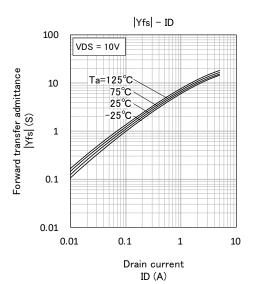




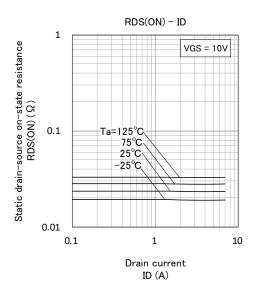


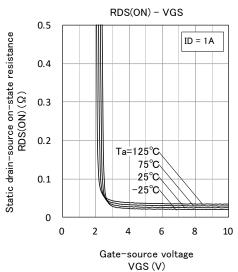


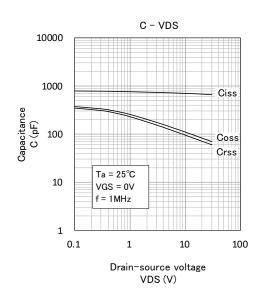


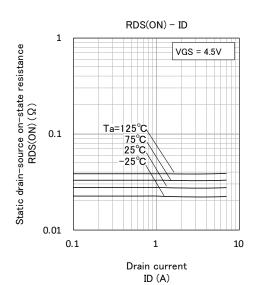


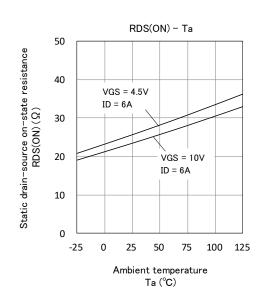
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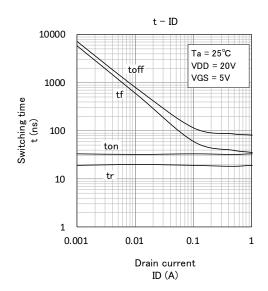




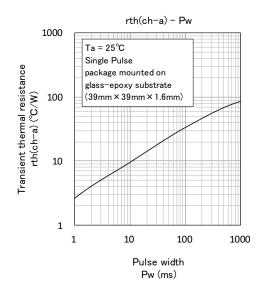


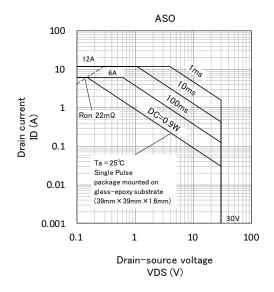


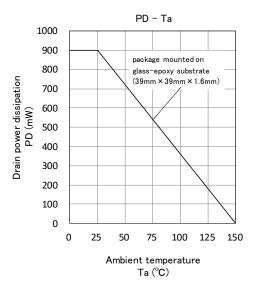




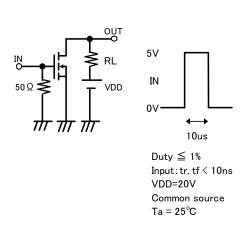
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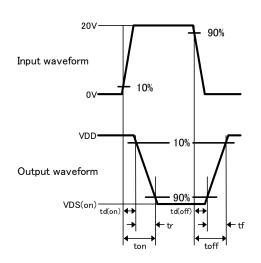






Switching time test condition





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