Built-in Zener Diode MOS field-effect transistor Silicon N-channel

AEC-Q101 Compliance

DESCRIPTION

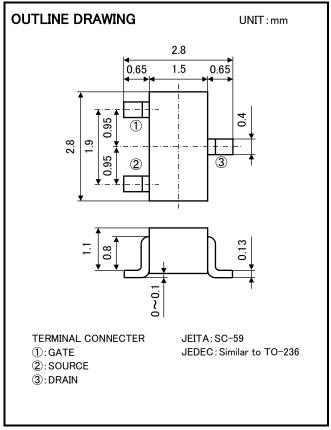
INKE211AC1 is a silicon N-channel MOS transistors with built-in Zener diode between drain and source, and small package (SC-59).

FEATURE

- •Low on Resistance. RDs(on)=250m Ω (TYP) @ID=500mA,VGs=4.5V RDs(on)=200m Ω (TYP) @ID=500mA,VGs=10V
- · High speed switching.
- •Drive voltage 4V
- •Built-in Zener diode between drain and source.
- · Large avalanche resistance.
- Small package for High-density packaging.

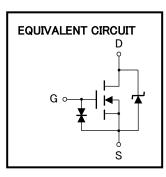
APPLICATION

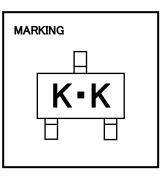
Motor drive, High-speed switching Analog switching, and others.



MAXIMUM RATINGS (Ta=25°C)

Symbol	Parameter	Rating	Unit	
Vgss	Gate-Source Voltage	±20	٧	
ĪD	ID Drain Current(DC) IDP Drain Current(Pulse) ※1		Α	
IDP			Α	
Do	F - 10 C:	200	mW	
Pb	Total Power Dissipation	550 (※2)	mW	
IAV	Avalanche Current ※3,※4	1.0	Α	
Eav	Eav Avalanche Energy ※3,※4		mJ	
Tch	Tch Channel Temperature		°C	
Tstg	Storage Temperature	−55 ~ +150	°C	





※1 : Pw≦1ms, Duty≦1%

X2: Package mounted on glass-epoxy substrate (20mm × 20mm × 1mm, Cu pad 100mm²)

3: Consecutive pulses Pw \leq 20 μ s, Duty \leq 0.2%

 $34 : L=100 \mu H$

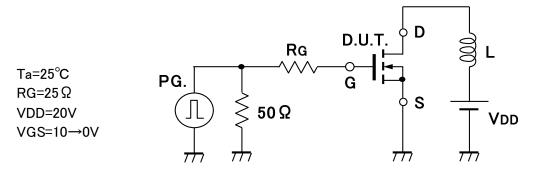
[MOSFET] ELECTRICAL CHARACTERISTICS (Ta=25°C)

Symbol	l Parameter	Test Condition	Limit			Unit
Symbol			MIN.	TYP.	MAX.	Unit
V(BR)DSS Drain-Source Breakdown Voltage		ID=100µA, VGS=0V	40	_	60	V
Igss	Gate-Source Leak current	Vgs=±20V, Vps=0V	-	_	±10	μA
I DSS	Zero Gate Voltage Drain Current	VDS=40V, VGS=0V	-	_	1	μΑ
V_{th}	Gate Threshold Voltage	ID=250µA, VDS=VGS	1.0	_	2.0	>
Yfs	Forward Transfer Admittance	VDS=10V, ID=500mA	-	1.5	_	S
RDS(ON)	Static Drain-Source On-State	ID=500mA, VGS=4.5V	-	250	-	mΩ
KD2(ON)	(ON) Resistance	ID=500mA, VGS=10V	-	200	_	111.75
Ciss	Input Capacitance	VDS=5V, VGS=0V, f=1MHz	-	170	_	pF
Coss	Output Capacitance		-	40	-	
ton	- Switching Time	VDD=5V, ID=250mA	_	170	_	
toff	- Switching Time	V _{GS} =0∼5V	_	85	_	ns

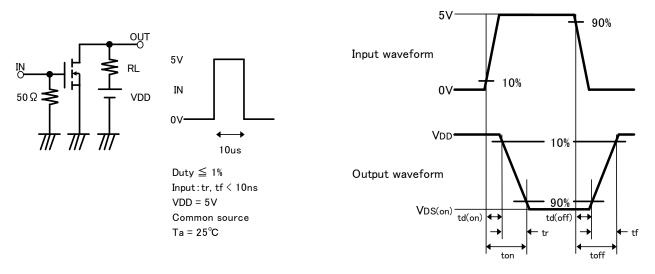
[Zener Diode] ELECTRICAL CHARACTERISTICS (Ta=25°C)

Zener Voltage Vz(V)			Reverse current IR(μ A)		
MIN	MAX	Iz(mA)	MAX	V _R (V)	
40	60	0.1	1.0	40	

Avalanche current test condition

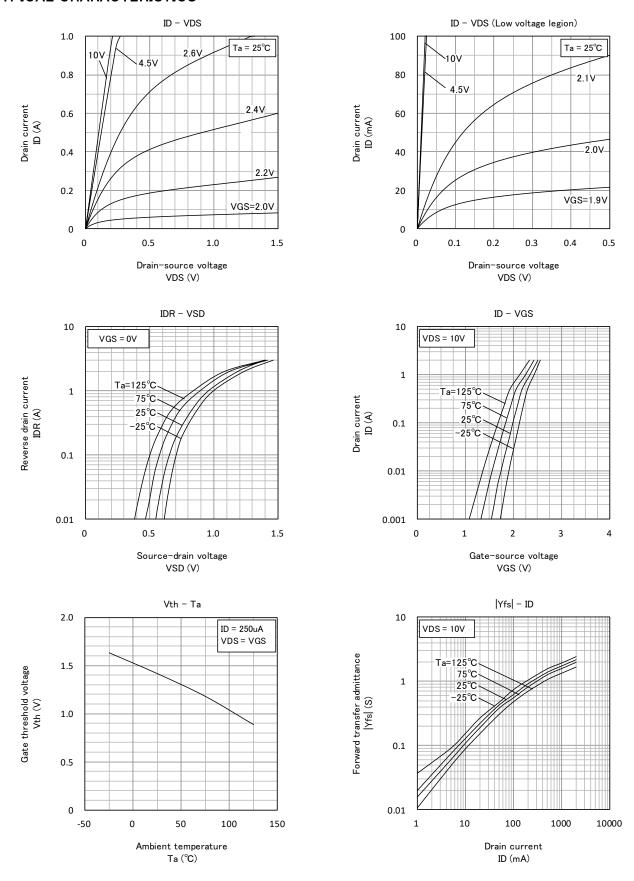


Switching time test condition

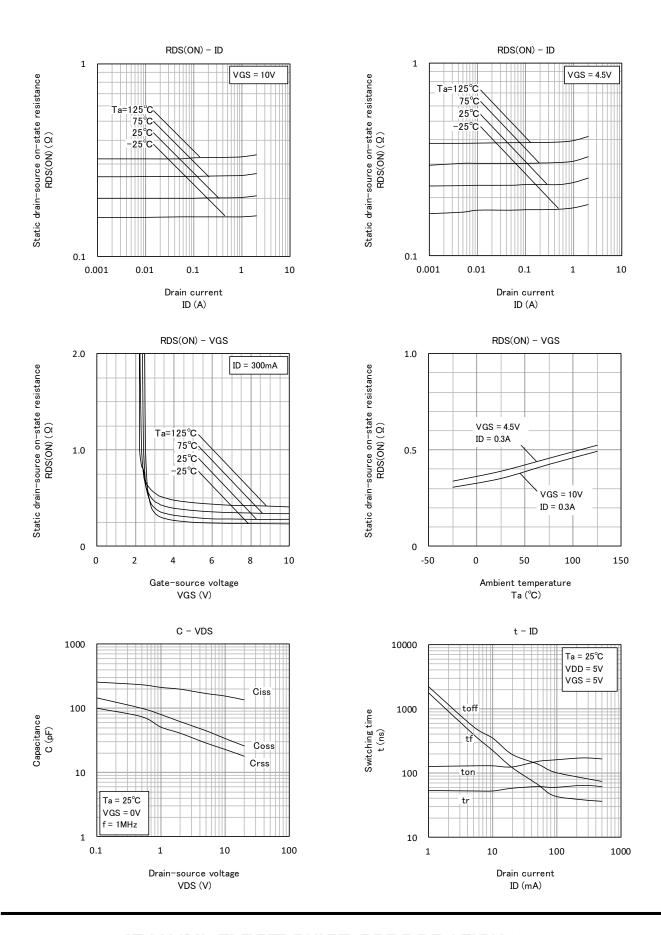


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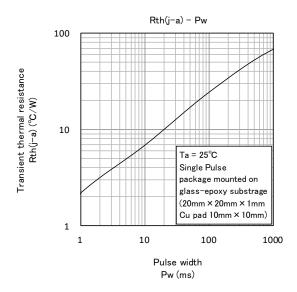
TYPICAL CHARACTERISTICS

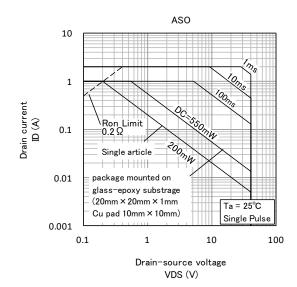


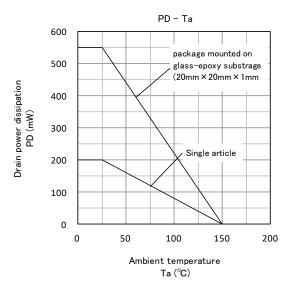
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