#### **DESCRIPTION**

M57174L-03 is a hybrid integrated circuit designed for driving n-channel IGBT modules that is able to connect its pins with PCB directly. This device provides the required electrical isolation between the input and output with each pulse transformers. This Hybrid IC can drive 3 IGBT elements by supply voltage of 2 lines (15V, 5V) and isolated type power supply is not necessary for each gate drivers.

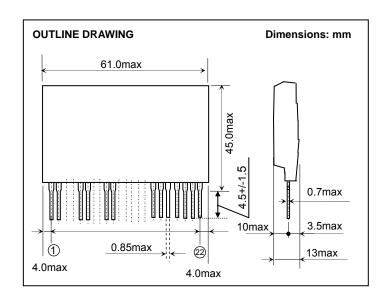
Recommended IGBT modules:

V<sub>CES</sub> = 600V series up to 50A class

V<sub>CES</sub> = 1200V series up to 25A class

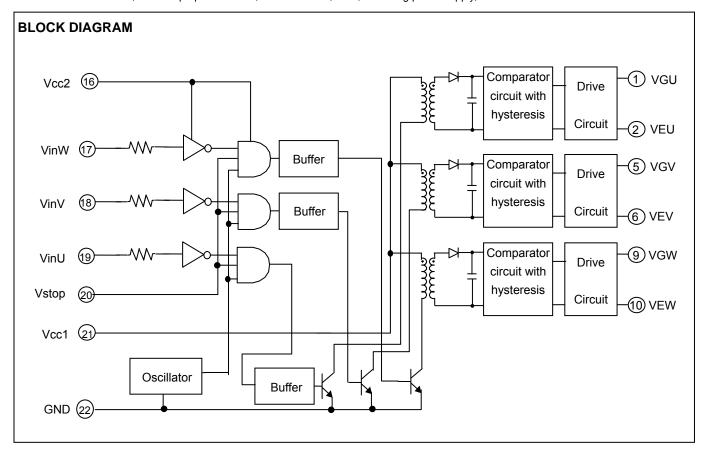
#### **FEATURES**

- Power supply for each gate driver is unnecessary (Adoption of pulse transformer method)
- •TTL, CMOS logic compatible input interface
- •Input-output isolation voltage ······· 2500Vrms, for 1 min
- •Each outputs isolation voltage······2500Vrms, for 1 min
- •SIL structure, and 3 drivers built-in



### **APPLICATIONS**

AC-DC motor controls, General purpose inverter, Servo controls, UPS, Switching power supply, etc.



# MAXIMUM RATINGS (unless otherwise noted, Ta=25°C)

Symbol	Parameter	Conditions	Ratings	Unit
V <sub>CC1</sub>	Supply voltage 1	DC, between pins 21 and 22	-0.3 ~ 16.5	V
V <sub>CC2</sub>	Supply voltage 2	DC, between pins 16 and 22	-0.5 ~ 5.5	V
V <sub>in</sub> , Vstop	Input/Error signal voltage	Between pins 17, 18, 19, 20 and 22	-0.5 ~ +6	V
I <sub>OHP</sub>	"H" output current	Pulse width:P <sub>W</sub> < 2µs	-0.6	Α
I <sub>OLP</sub>	"L" output current	Pulse width:Pw < 2µs	0.6	Α
V <sub>iso1</sub>	Isolation voltage1	Sine wave,60Hz, 1min. ,R.H.<50%	2500	Vrms
V <sub>iso2</sub>	Isolation voltage2	Sine wave,60Hz, 1min. ,R.H.<50%	2500	Vrms
T <sub>C</sub>	Case temperature		85	°C
Topr	Operating temperature	No condensation allowable	-20 ~ +60	°C
Tstg	Storage temperature	No condensation allowable	-25 ~ +100	°C

# **ELECTRICAL CHARACTERISTICS**

(Unless otherwise noted, Ta=25°C,  $V_{CC1}$  = 15.0V,  $V_{CC2}$  = 5.0V, f = 20kHz, D.F. = 50%, RG=13 ohm ,CL = 4700pF)

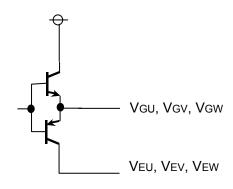
Symbol	Item	Conditions	Limits			l lmit
		Conditions	Min	Тур	Max	Unit
V <sub>CC1</sub>	Supply voltage 1	Recommended range	14.5	15.0	16.0	V
V <sub>CC2</sub>	Supply voltage 2	Recommended range	4.75	5.0	5.25	V
f	Switching frequency	Recommended range	-	-	20	kHz
RG	Gate resistor	Recommended range	13	-	-	Ω
$V_{in(ON)}$	"H" level input threshold voltage	Vin	-	-	0.64	V
$V_{in(OFF)}$	"L" level input threshold voltage	Vin	3.5	-	-	V
V <sub>OH</sub>	"H" output voltage	V <sub>in</sub> : "L" and V <sub>STOP</sub> : "H"	13.5	14.8	-	V
$V_{OL}$	"L" output voltage	V <sub>in</sub> : "H or V <sub>STOP</sub> : "L"	-	0.6	1.4	٧
t <sub>PLH</sub>	"L-H" Propagation time (*1)	V <sub>in</sub> : "L" and V <sub>STOP</sub> : "H"	-	1.3	2.2(*2)	μs
t <sub>r</sub>	"L-H" Rise time	V <sub>in</sub> : "L" and V <sub>STOP</sub> : "H"	-	0.2	0.6	μs
t <sub>PHL</sub>	"H-L" Propagation time (*1)	V <sub>in</sub> : "H" or V <sub>STOP</sub> : "L"	-	1.0	2.0(*2)	μs
t <sub>f</sub>	"H-L" fall time	V <sub>in</sub> : "H" or V <sub>STOP</sub> : "L"	-	0.4	0.8	μs

<sup>(\*1)</sup> Depending on the timing of input signal and internal oscillation circuit, the propagation time changes in the extent of  $\pm -0.4 \mu s$ .

# **DIFINITION OF CHARACTERISTICS**

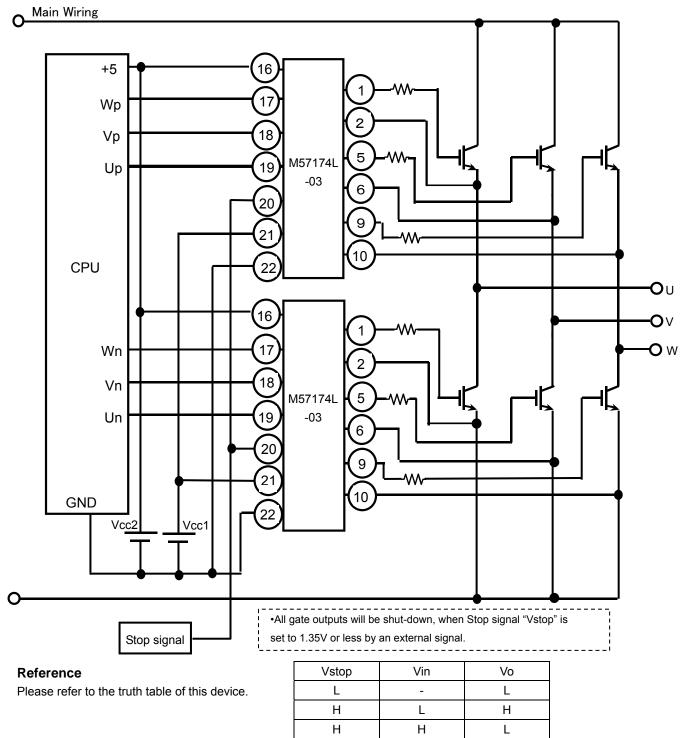
# Vin $V_{in(OFF)}$ Vo $V_{OH}$ $V_$

# **OUTPUT CIRCUIT**



<sup>(\*2)</sup>tPLH=2.2us and tPHL=2.0us at maximum limit: Those are contained the changing value 0.4us by timing of (\*1), so each center value is tPLH=1.8us and tPHL=1.6us at minimum limit.

# **APPLICATION CIRCUIT**



#### **PRECAUTION**

In case that , wiring between an inverter output and a load motor is very long such as 10m or more, this driver may output "H" voltage unexpectedly for about 1 us by dv / dt noise when the other IGBT turned on, whose IGBT modules are constructed in same arm. If this problem occur, then two measures will be effective ,that are shown as follows,

- (1) Connect capacitors of 0.01uF with terminals between gate and emitter on each IGBT module.
- (2) Setting up a dead time more than 5us.

### FOR SAFETY USING

Great detail and careful attention are given to the production activity of Hics, such as the development, the quality of production, and in it's reliability. However the reliability of Hics depends not only on their own factors but also in their condition of usage. When handling Hics, please note the following cautions.

CAUTIONS				
Packing	The materials used in packing Hics can only withstand normal external conditions.  When exposed to outside shocks, rain and certain environmental contaminators, the packing materials will deteriorates. Please take care in handling.			
Carrying	<ol> <li>Don't stack boxes too high. Avoid placing heavy materials on boxes.</li> <li>Boxes must be positioned correctly during transportation to avoid breakage.</li> <li>Don't throw or drop boxes.</li> <li>Keep boxes dry. Avoid rain or snow.</li> <li>Minimal vibration and shock during transportation is desirable.</li> </ol>			
Storage	<ul> <li>When storing Hics, please observe the following notices or possible deterioration of their electrical characteristics, risk of solderability, and external damage may occur.</li> <li>1) Devices must be stored where fluctuation of temperature and humidity is minimal, and must not be exposed to direct sunlight. Store at the normal temperature of 5 to 30 degrees Celsius with humidity at 40 to 60%.</li> <li>2) Avoid locations where corrosive gasses are generated or where much dust accumulates.</li> <li>3) Storage cases must be static proof.</li> <li>4) Avoid putting weight on boxes.</li> </ul>			
Extended storage	When extended storage is necessary, Hics must be kept non-processed. When using Hics which have been stored for more than one year or under severe conditions, be sure to check that the exterior is free from flaw and other damages.			
Maximum ratings	To prevent any electrical damages, use Hics within the maximum ratings. The temperaqture, current, voltage, etc. must not exceed these conditions.			
Polarity	To protect Hics from destruction and deterioration due to wrong insertion, make sure of polarity in inserting leads into the board holes, conforming to the external view for the terminal arrangement.			



Marketing division, Marketing plannning department 6-41 Tsukuba, Isahaya, Nagasaki, 854-0065 Japan

## Keep safety first in your circuit designs!

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