RTAN140X SERIES

Transistor With Resistor For Muting Application Silicon NPN Epitaxial Type

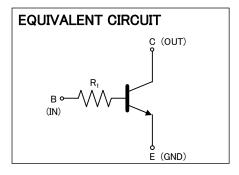
FEATURE

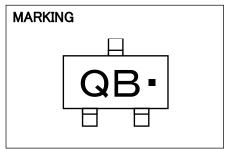
- •Built-in bias resistor (R_1 =10k Ω).
- ·Small package for easy mounting.
- · High reverse h_{FE}.
- Small collector to emitter saturation voltage. $V_{CE(sat)} = 10 \text{mV}_{(TYP.)} (@I_C = 10 \text{mA}/I_B = 0.5 \text{mA})$
- ·Low on Resistor.

 $R_{ON}=0.94 \Omega_{(TYP)}(@V_I=7V)$

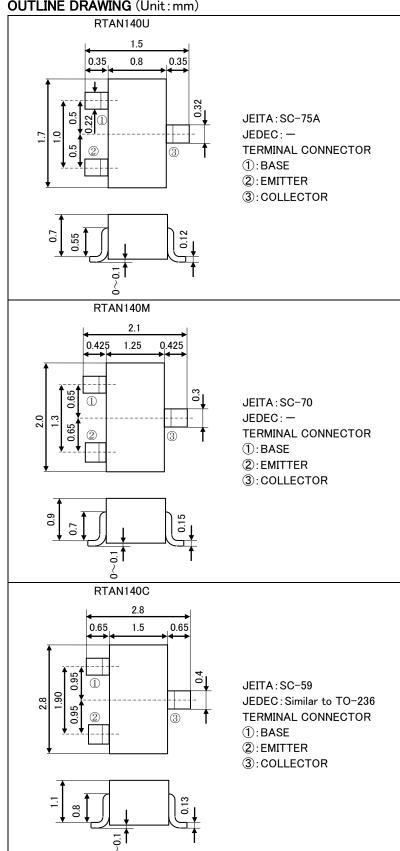
APPLICATION

muting circuit, switching circuit





OUTLINE DRAWING (Unit:mm)



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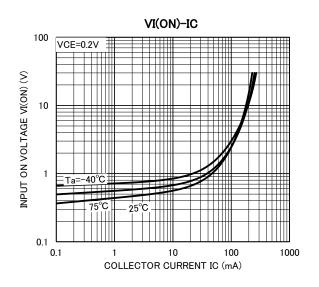
MAXIMUM RATING (Ta=25°C)

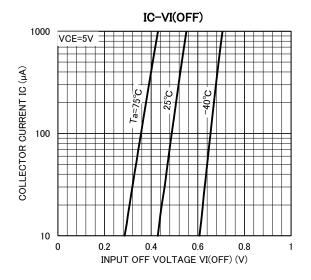
PARAMETER	SYMBOL	RATING			
		RTAN140U	RTAN140M	RTAN140C	UNIT
Collector to Base voltage	V _{CBO}		40		٧
Emitter to Base voltage	V _{EBO}		40		٧
Collector to Emitter voltage	V _{CEO}		20		٧
Collector current	I _C		400		mA
Collector dissipation	Pc	150	20	mW	
Junction temperature	T _j		+150		°C
Storage temperature	T _{stg}		−55 ~ +150		°C

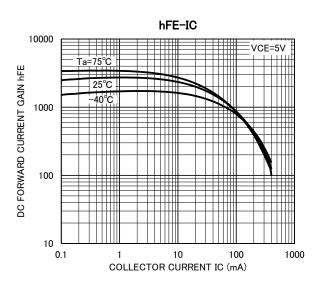
ELECTRICAL CHARACTERISTICS (Ta=25°C)

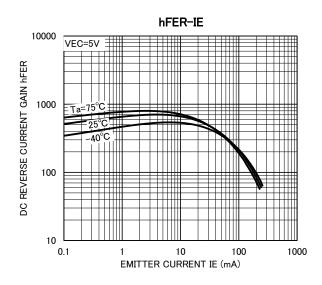
PARAMETER	CVMPOL	TEGT COMPLETION	LIMIT			LINIT
	SYMBOL	TEST CONDITION	MIN	TYP	MAX	UNIT
C to B breakdown voltage	V _{(BR)CBO}	$I_C=50 \mu A$, $I_E=0mA$	40	_	_	V
E to B breakdown voltage	$V_{(BR)EBO}$	$I_E=50 \mu A$, $I_C=0mA$	40	-	-	V
C to E breakdown voltage	$V_{(BR)CEO}$	I _C =1mA, R _{BE} =∞	20	-	_	V
Collector cut off current	I _{CBO}	V _{CB} =40V, I _E =0mA	_	-	0.5	μΑ
Emitter cut off current	I _{EBO}	V _{EB} =40V, I _C =0mA	_	-	0.5	μΑ
DC forward current gain	h _{FE}	$V_{CE}=5V$, $I_{C}=10$ mA	820	-	2500	_
C to E saturation voltage	$V_{CE(sat)}$	I _C =10mA, I _B =0.5mA	_	10	-	mV
Input resistor	R ₁	_	7	10	13	kΩ
Gain band width product	f _T	V _{CE} =10V, I _E =-10mA, f=100MHz	_	35	_	MHz
Output "ON" resistor	Ron	V_{I} =7 V , R_{L} =1 $k\Omega$	_	0.94	_	Ω

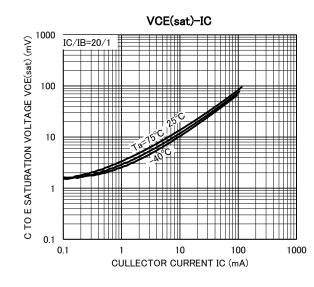
TYPICAL CHARACTERISTICS

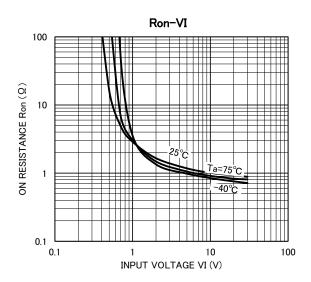












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