(SMALL-SIGNAL TRANSISTOR)

RTGN432P

TRANSISTOR WITH RESISTOR FOR SWITHING APPLICATION SILICON NPN EPITAXIAL TYPE

OUTLINE DRAWING Unit:mm DISCRIPTION RTGN432P is a one chip transistor with 4.4 built-in bias transistor. 1.6 FEATURE • Built-in bias resistor (R1= $4.7k\Omega$,R2= $10k\Omega$) 0 1 3 High collector current IC=1A Built-in zener diode between collector and base 2₽ 0.4 0.4 APPLICATION 1.5 Motor driver circuit MARKING 3.0 TERMINAL CONNECTOR JEITA: SC-62 $\textcircled{1}:\mathsf{BASE}$ JEDEC: SOT-89 2:COLLECTOR 3: EMITTER EQUIVALENT CIRCUIT MARKING Type name oc Ν ⊢ Óf The last number Marking month Running No. of fiscal year

MAXIMUM RATING(Ta=25°C)

SYMBOL	PARAMETER	RATING	UNIT
Vcbo	Collector to Base voltage	60±10	V
Vebo	Emitter to Base voltage	10	V
$V_{\rm CEO}$	Collector to Emitter voltage	60±10	V
$I_{\rm C}$	Collector current (DC)	1	А
I _{CM}	Collector current (pulse)	2	А
P_{C}	Collector dissipation	500	mW
T_{j}	Junction temperature	+150	°C
$\mathrm{T}_{\mathrm{stg}}$	Storage temperature	-55~+150	°C

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ELECTRICAL CHARACTERISTICS (Ta=25°C)

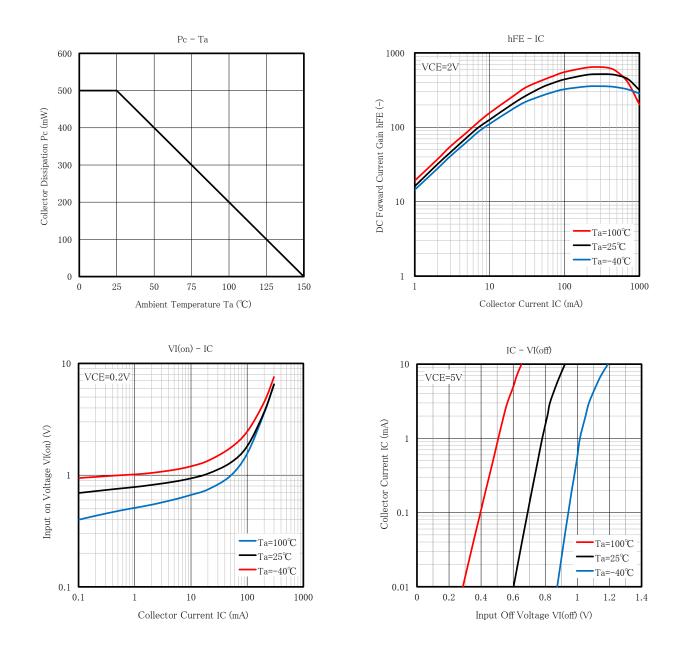
Symbol	Parameter	Test conditions	Limits			Unit
	rarameter	Test conditions		Тур	Max	Unit
$I_{\rm CBO}$	Collector cut off current	V _{CB} =40V, I _E =0		I	0.1	μA
Vol	Output voltage	V _I =5V, I _C =200mA	_	-	0.2	V
VIL	Input voltage (OFF)	$V_{CE}=5V$, $I_{C}=100 \mu A$	0.3	I	_	V
$\mathbf{h}_{\mathrm{FE1}}$	DC forward current gain	V _{CE} =2V, I _C =0.1A	200		—	
h_{FE2}	DC forward current gain	V _{CE} =2V, I _C =0.5A	300		_	_
h _{FE3}	DC forward current gain	V _{CE} =2V, I _C =1A	200		_	
R_1	Input resistor	_	3.29	4.70	6.11	kΩ
$ m R_2$	Emitter – Base resistor	-	7	10	13	kΩ

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TYPICAL CHARACTERISTICS



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