RT3CLLM

Composite Transistor For Low Frequency Amplify Application Silicon NPN Epitaxial Type

DESCRIPTION

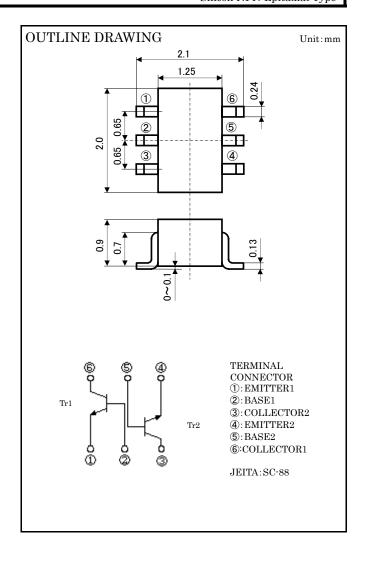
RT3CLLM is compound transistor built with two $2SC3052\ chips$ in $SC\text{-}88\ package$.

FEATURE

Silicon NPN epitaxial type
Each transistor elements are independent.
Mini package for easy mounting

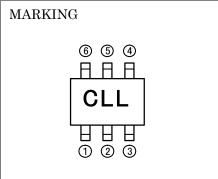
APPLICATION

For low frequency amplify application



MAXIMUM RATING (Ta=25°C) (Tr1, Tr2)

SYMBOL	PARAMETER	RATING	UNIT	
Vcbo	Collector to Base voltage	50	V	
VEBO	Emitter to Base voltage	6	V	
$V_{\rm CEO}$	Collector to Emitter voltage	50	V	
Ic	Collector current	200	mA	
PT	Total dissipation(Ta=25°C)	200	mW	
$T_{\rm j}$	Junction temperature	+150	လူ	
$T_{ m stg}$	Storage temperature	-55~+150	°C	



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ELECTRICAL CHARACTERISTICS (Ta=25°C) (Tr1, Tr2)

Symbol	Parameter	Test conditions	Limits			Unit
			Min	Typ	Max	Onit
V(BR)CEO	Collector to Emitter breakdown voltage	I _C =100 μ A,R _{BE} =∞	50	-	-	V
Ісво	Collector cut off current	V _{CB} =50V,I _E =0	-	-	0.1	μΑ
IEBO	Emitter cut off current	V_{EB} =6 V_{IC} =0	-	-	0.1	μΑ
hFE*	DC forward current gain	V _{CE} =6V,I _C =1mA	150	-	500	-
hfe	DC forward current gain	V _{CE} =6V,I _C =0.1mA	90	-	-	-
VCE(sat)	Collector to Emitter saturation voltage	Ic=100mA,IB=10mA	-	-	0.3	V
$ m f_{T}$	Gain band width product	V _{CE} =6V,I _E =-10mA	-	200	-	MHz
Cob	Collector output capacitance	V _{CB} =6V,I _E =0,f=1MHz	-	2.5	-	pF
NF	Noise figure	V_{CE} =6 V_{IE} =-0.1 mA_{f} =1 kHz_{RG} =2 $k\Omega$	-	-	15	dB

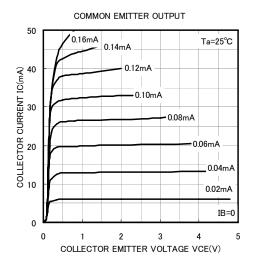
^{*:} It shows hee classification in right table.

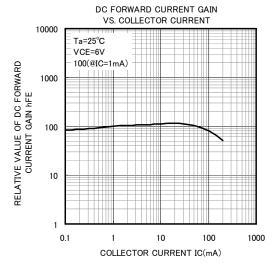
Item	E	F
$_{ m hFE}$	150~300	250~500

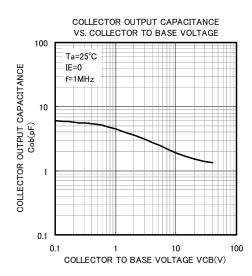
RT3CLLM

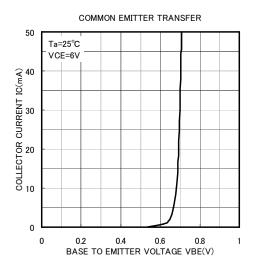
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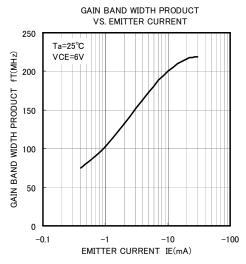
TYPICAL CHARACTERISTICS (Tr1, Tr2)













Keep safety first in your circuit designs!

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