RT2N02M

Composite Transistor With Resistor For Switching Application Silicon NPN Epitaxial Type

DESCRIPTION

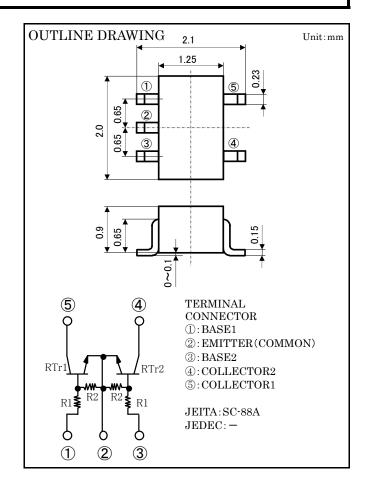
RT2N02M is composite transistor with built-in bias resistor.

FEATURE

Built-in bias resistor (R1=4.7k Ω , R2=4.7k Ω) Mini package for easy mounting

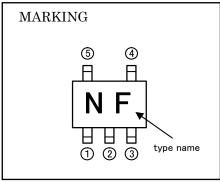
APPLICATION

Inverted circuit, Switching circuit, Interface circuit, Driver circuit



MAXIMUM RATING(Ta=25°C)(RTr1, RTr2 COMMON)

SYMBOL	PARAMETER	RATING	UNIT	
Vcbo	Collector to Base voltage	50	V	
VEBO	Emitter to Base voltage	10	V	
V_{CEO}	Collector to Emitter voltage	50	V	
V_{IN}	Input voltage	30	V	
Ic	Collector current	100	mA	
Icm	Peak Collector current	200	mA	
PT	Total dissipation	200	mW	
Tj	Junction temperature	+150	°C	
$T_{ m stg}$	Storage temperature	-55~+150	°C	



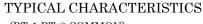
${\bf ELECTRICAL\ CHARACTERISTICS} (Ta=25^{\circ}{\bf c}) (RTr1,\ RTr2\ COMMON)$

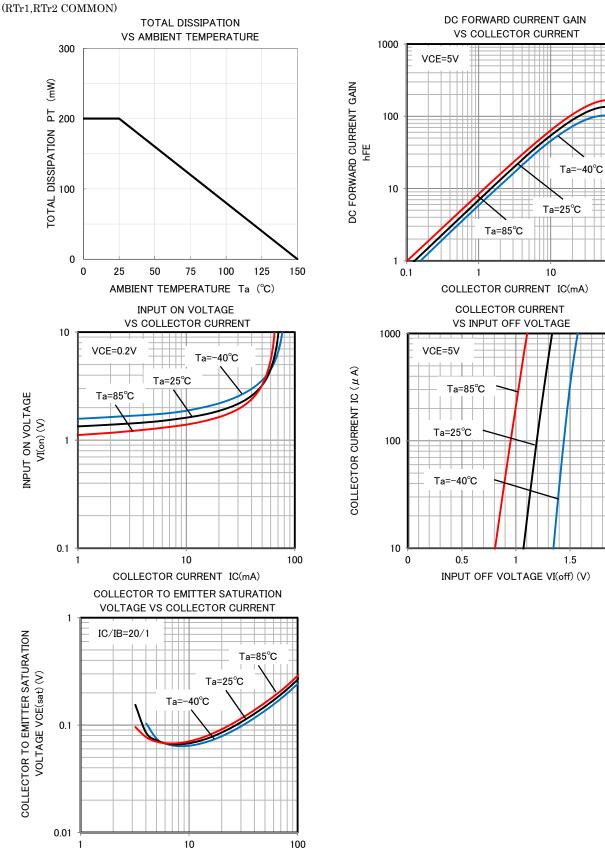
SYMBOL	PARAMETER	TEST CONDITIONS	LIMITS			TINITE
			MIN	TYP	MAX	UNIT
V(BR)CEO	Collector to Emitter breakdown voltage	I _C =100 μ A, R _{BE} =∞	50	_	_	V
Icbo	Collector cut off current	V _{CB} =50V, I _E =0	_	_	0.1	μΑ
IEBO	Emitter cut off current	V_{EB} =5V, I_C =0	399	532	771	μΑ
$_{ m hFE}$	DC forward current gain	V_{CE} =5 V , I_{C} =10 m A	20	_	_	_
VCE(sat)	Collector to Emitter saturation voltage	$I_C=10$ mA, $I_B=0.5$ mA	_	_	0.3	V
$V_{\rm I(ON)}$	Input on voltage	V _{CE} =0.2V, I _C =5mA	_	1.4	2.3	V
$V_{\rm I(OFF)}$	Input off voltage	$V_{\rm CE} = 5 V$, $I_{\rm C} = 100 \mu {\rm A}$	0.8	1.1	_	V
R_1	Input resistor	_	3.3	4.7	6.1	kΩ
R_2/R_1	Resistor ratio	_	0.8	1.0	1.2	_
f_{T}	Gain band width product	V _{CE} =6V, I _E =-10mA	_	200	_	MHz

RT2N02M

100

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COLLECTOR CURRENT IC(mA)



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