

RT3TUUM-T150

Composite Transistor With Resistor
For Switching Application
Silicon Epitaxial Type

AEC-Q101 Compliance

DESCRIPTION

RT3TUUM is composite transistor built with RT1N14B chip and RT1P14B chip in SC-88 package.

FEATURE

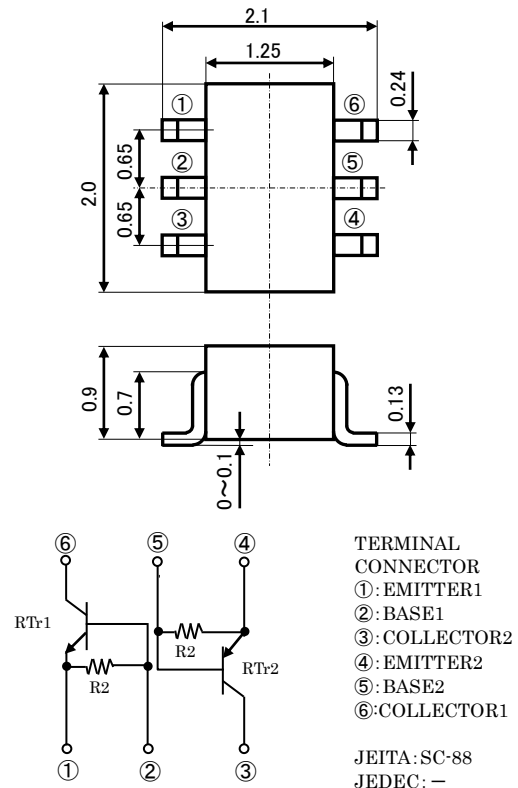
Built-in bias resistor (R2=10k Ω)
Mini package for easy mounting

APPLICATION

Inverted circuit, Switching circuit,
Interface circuit, Driver circuit

OUTLINE DRAWING

Unit:mm

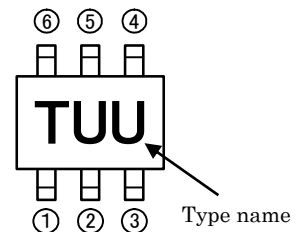


MAXIMUM RATING (Ta=25°C) (RTTr1_NPN, RTTr2_PNP)

SYMBOL	PARAMETER	RATING	UNIT
VCBO	Collector to Base voltage	50	V
VEBO	Emitter to Base voltage	6	V
VCEO	Collector to Emitter voltage	50	V
IC	Collector current	100	mA
ICM	Peak Collector current	200	mA
PT	Total dissipation	200	mW
T _j	Junction temperature	+150	°C
T _{stg}	Storage temperature	-55~+150	°C

※PNP built in transistor of "—"sign is abbreviation.

MARKING



ELECTRICAL CHARACTERISTICS (Ta=25°C) (RTTr1_NPN, RTTr2_PNP)

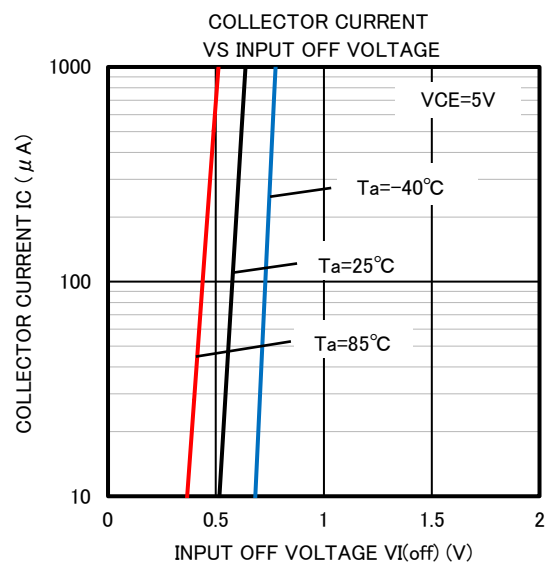
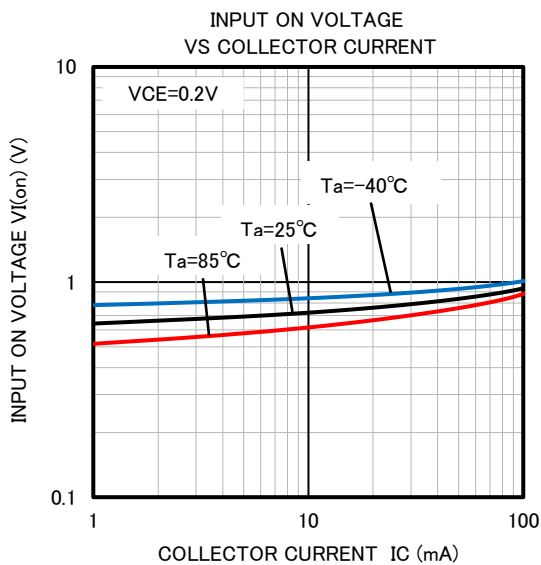
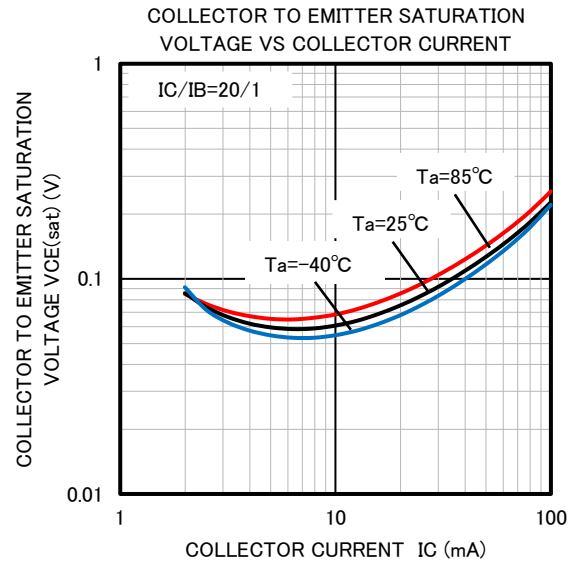
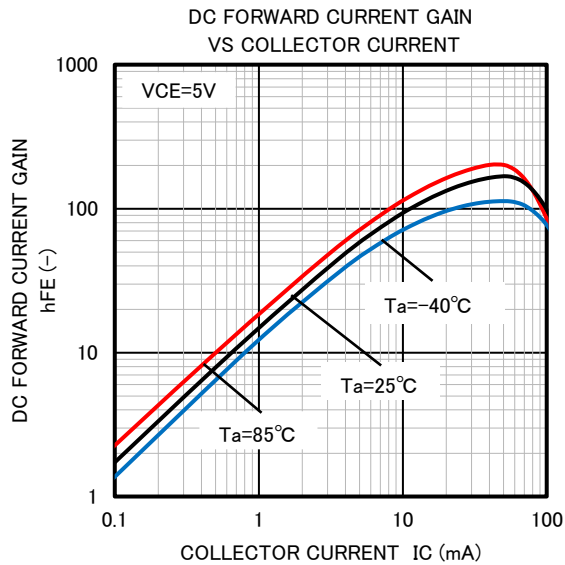
Symbol	Parameter	Test conditions	Limits			Unit
			Min	Typ	Max	
V(BR)CEO	Collector to Emitter breakdown voltage	I _C =100 μ A, R _{BE} = ∞	50	—	—	V
ICBO	Collector cut off current	V _{CB} =50V, I _E =0	—	—	0.1	μ A
IEBO	Emitter cut off current	V _{EB} =5V, I _C =0	375	500	725	μ A
hFE	DC forward current gain	V _{CE} =5V, I _C =5mA	30	—	—	—
VCE(sat)	Collector to Emitter saturation voltage	I _C =10mA, I _B =0.5mA	—	—	0.3	V
R2	Emitter to Base resistor	—	7	10	13	k Ω
f _T	Gain band width product	V _{CE} =6V, I _E =10mA	RTTr1	—	200	MHz
			RTTr2	—	150	

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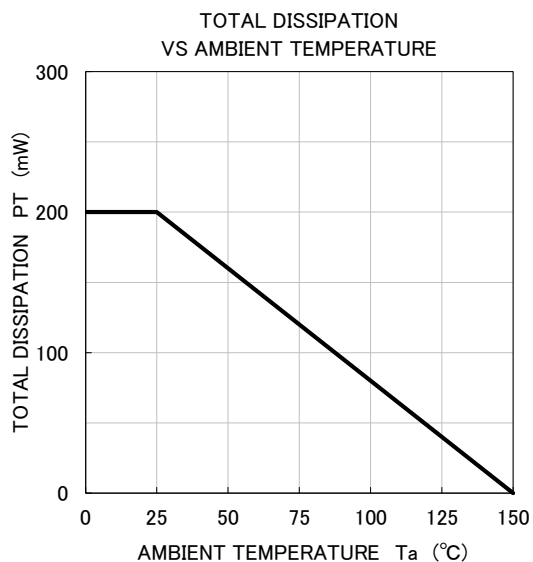
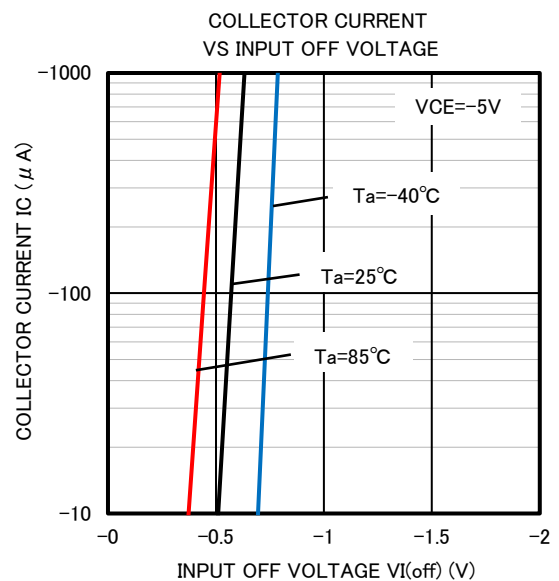
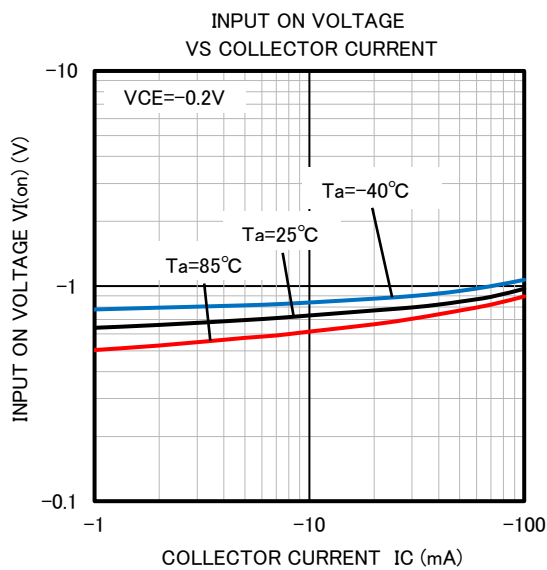
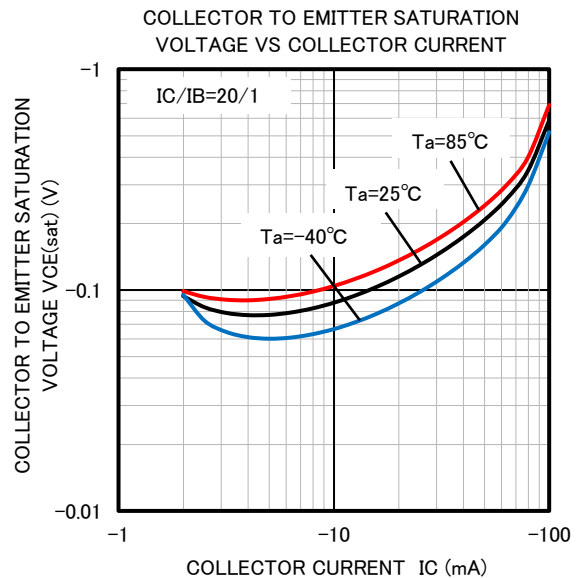
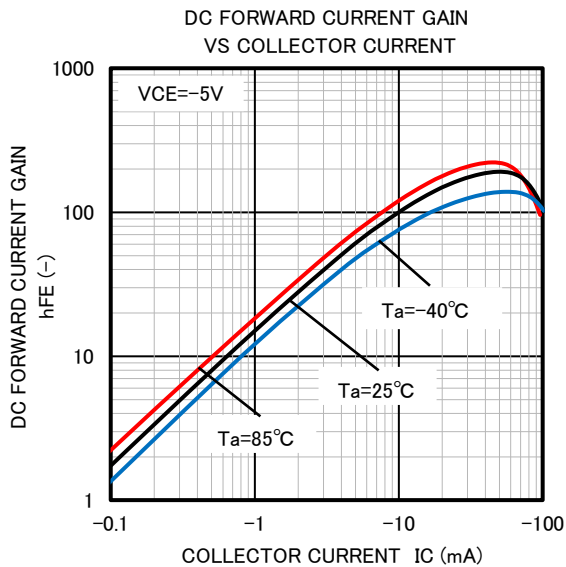
TYPICAL CHARACTERISTICS (RT_r1_NPN)



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TYPICAL CHARACTERISTICS (RT_r 2_PNP)



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